

Fig. 1

1002262-122001

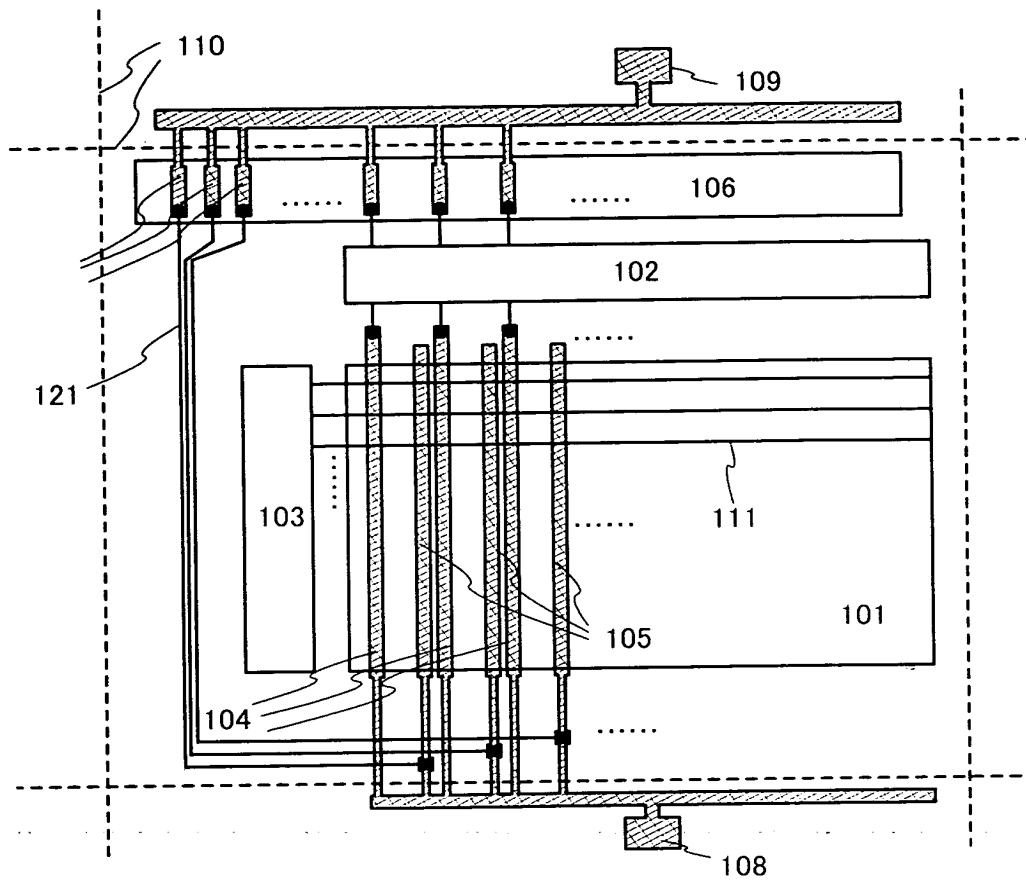
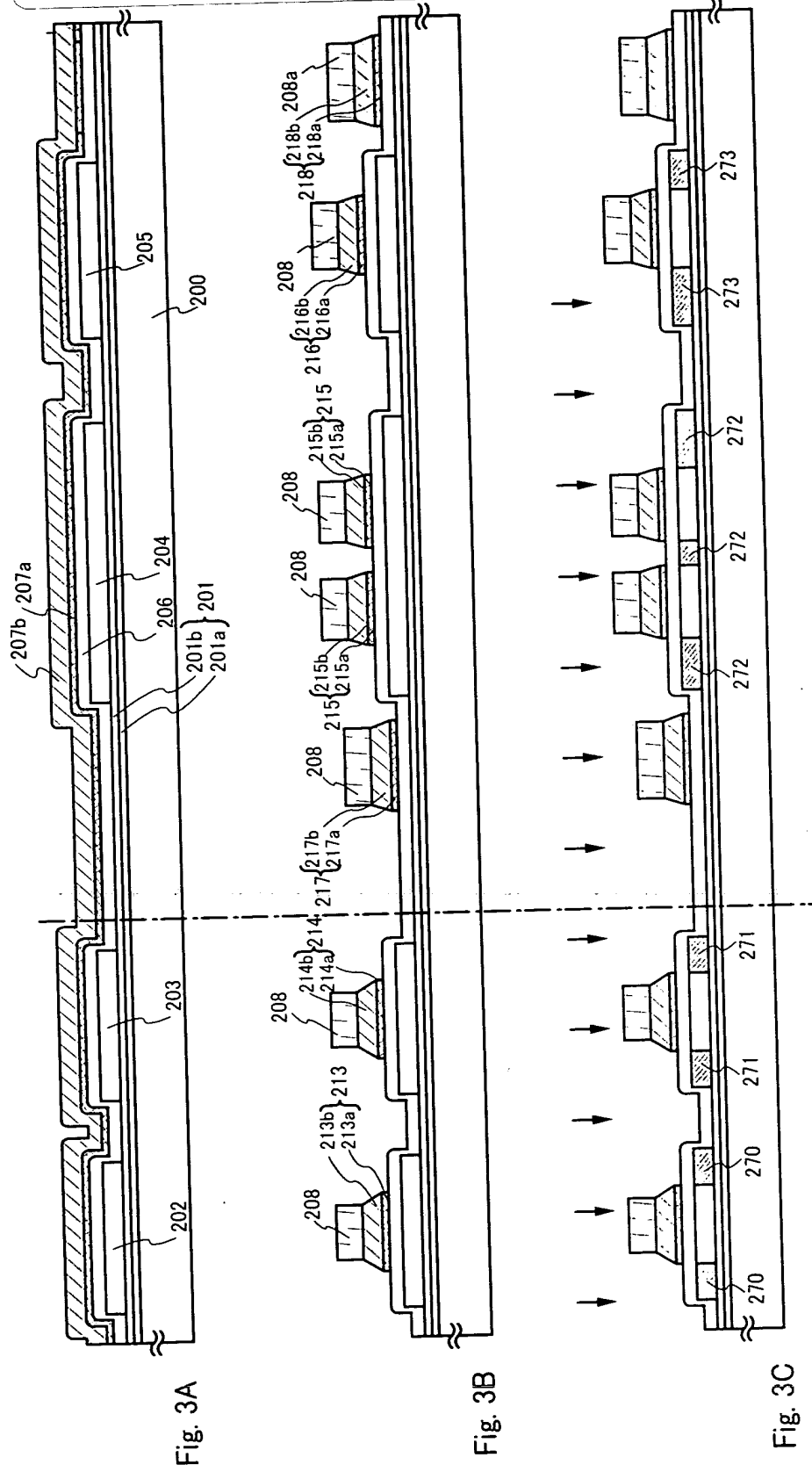


Fig. 2

SECRET



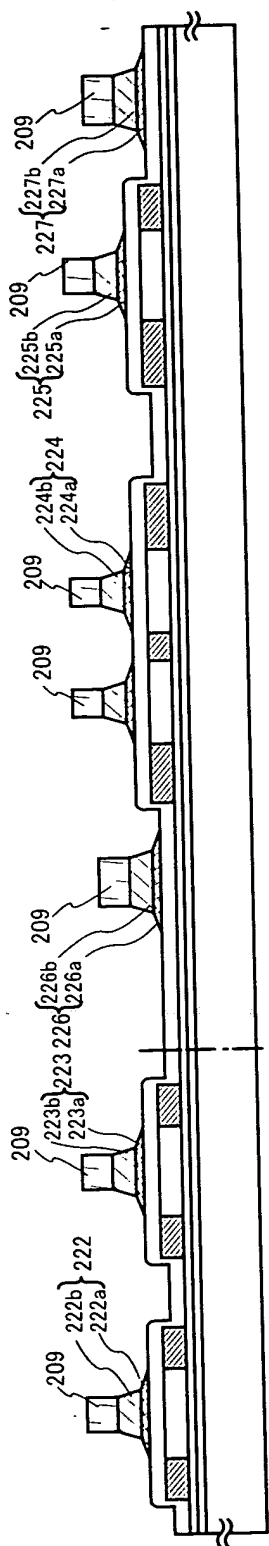


Fig. 4A

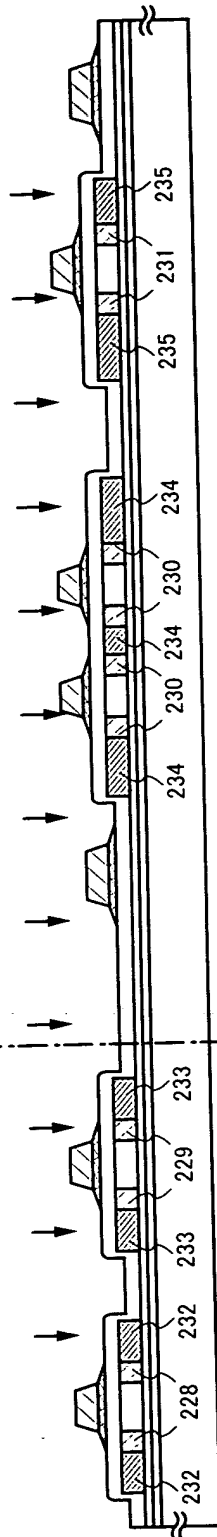


Fig. 4B

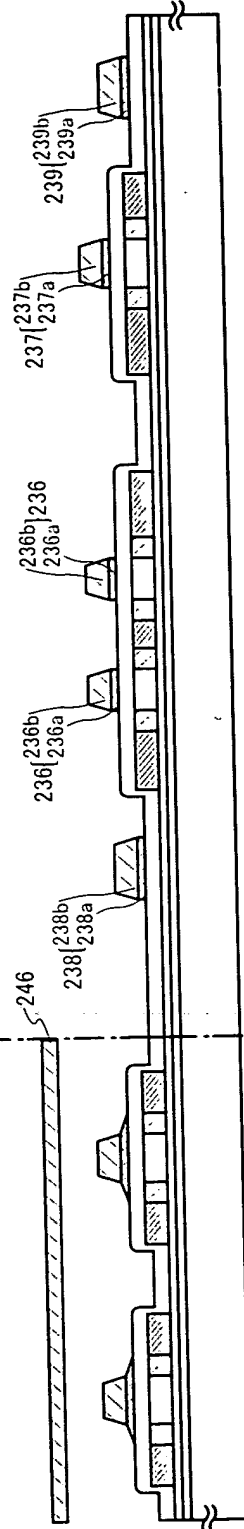


Fig. 4C

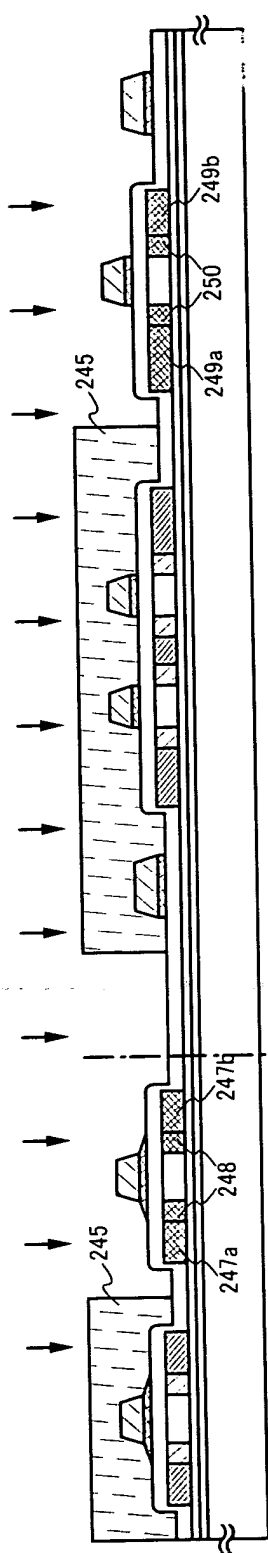


Fig. 5A

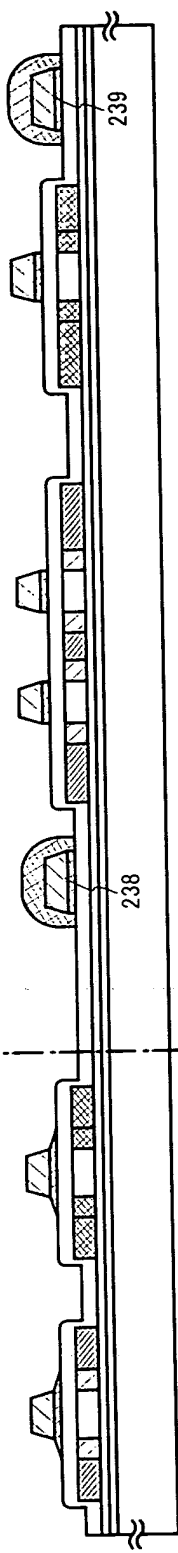


Fig. 5B

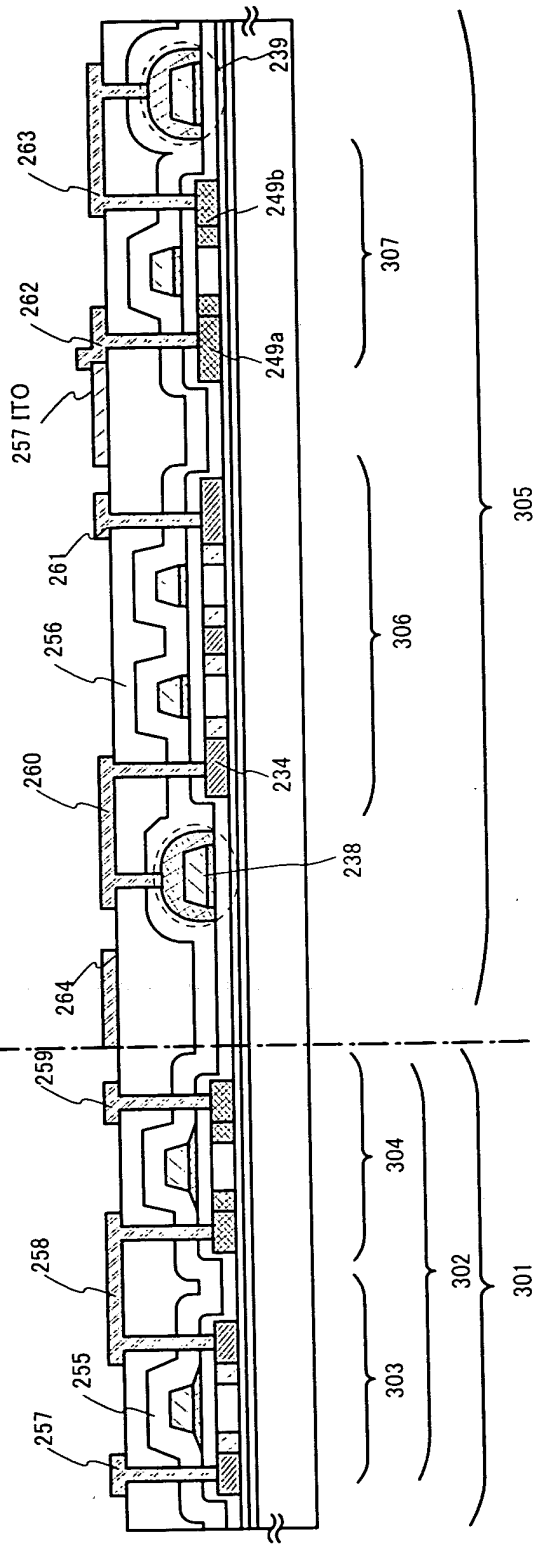


Fig. 5C

100221" 29222001

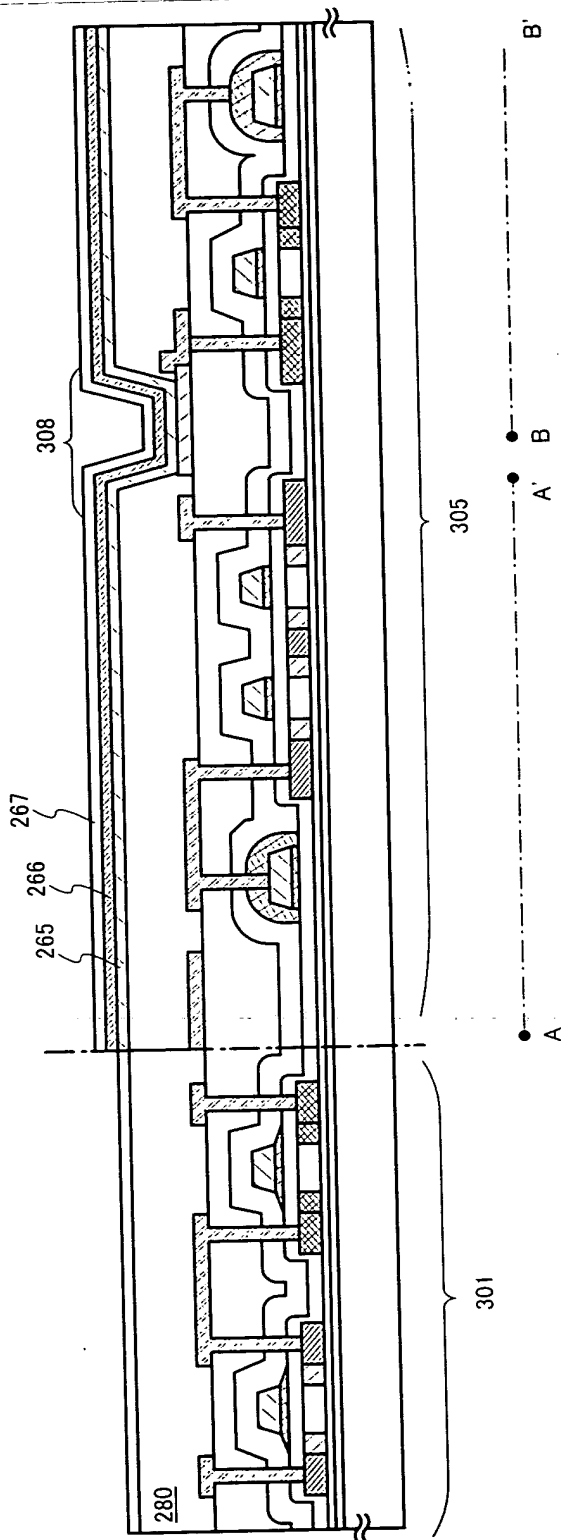


Fig. 6

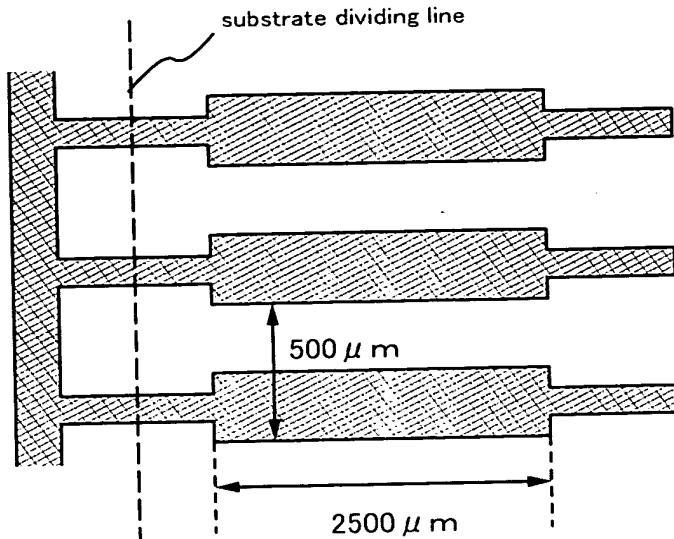


Fig. 7A

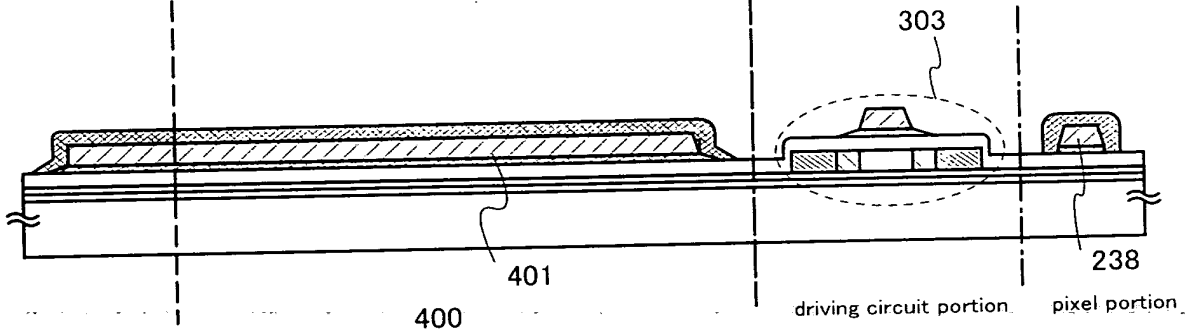


Fig. 7B

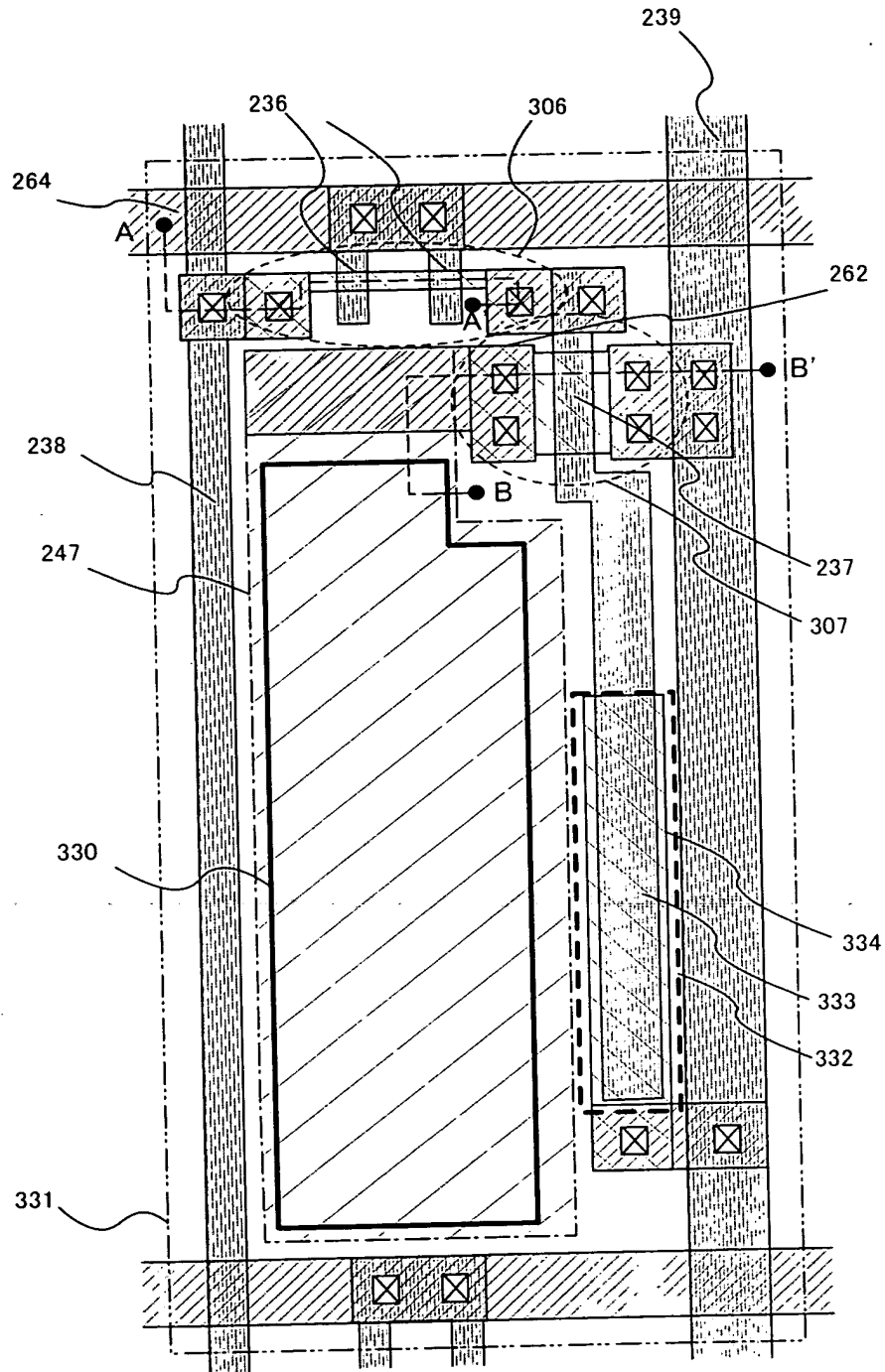


Fig. 8

1002221" 29222001

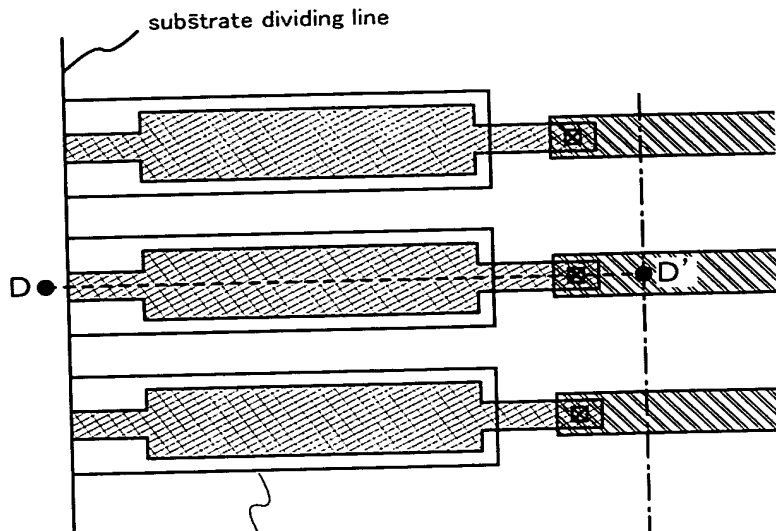


Fig. 9A

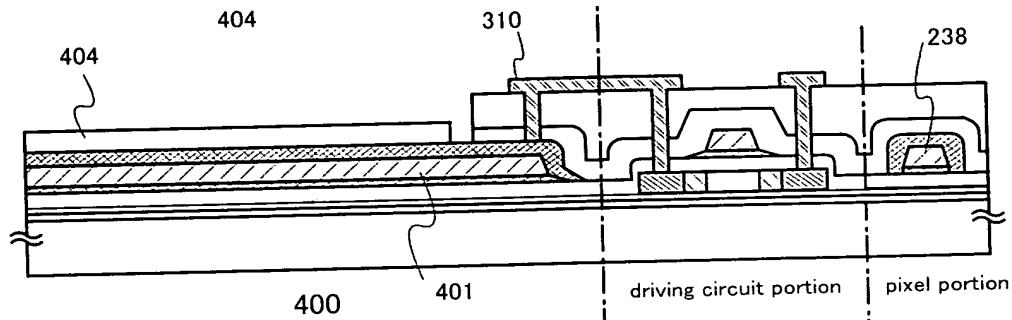


Fig. 9B

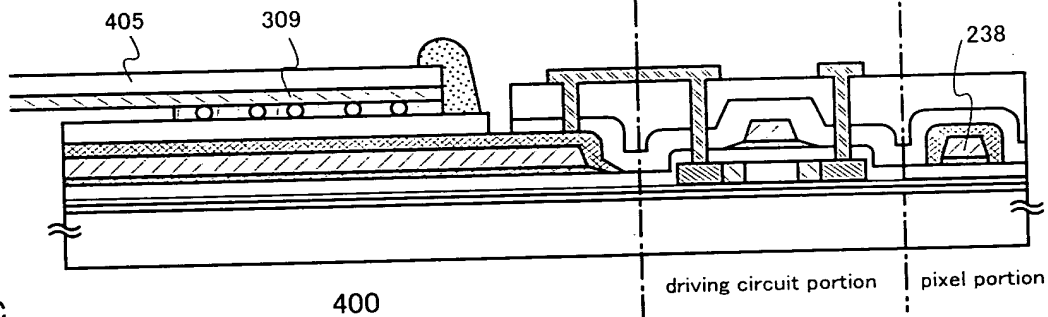


Fig. 9C

1002262-12001

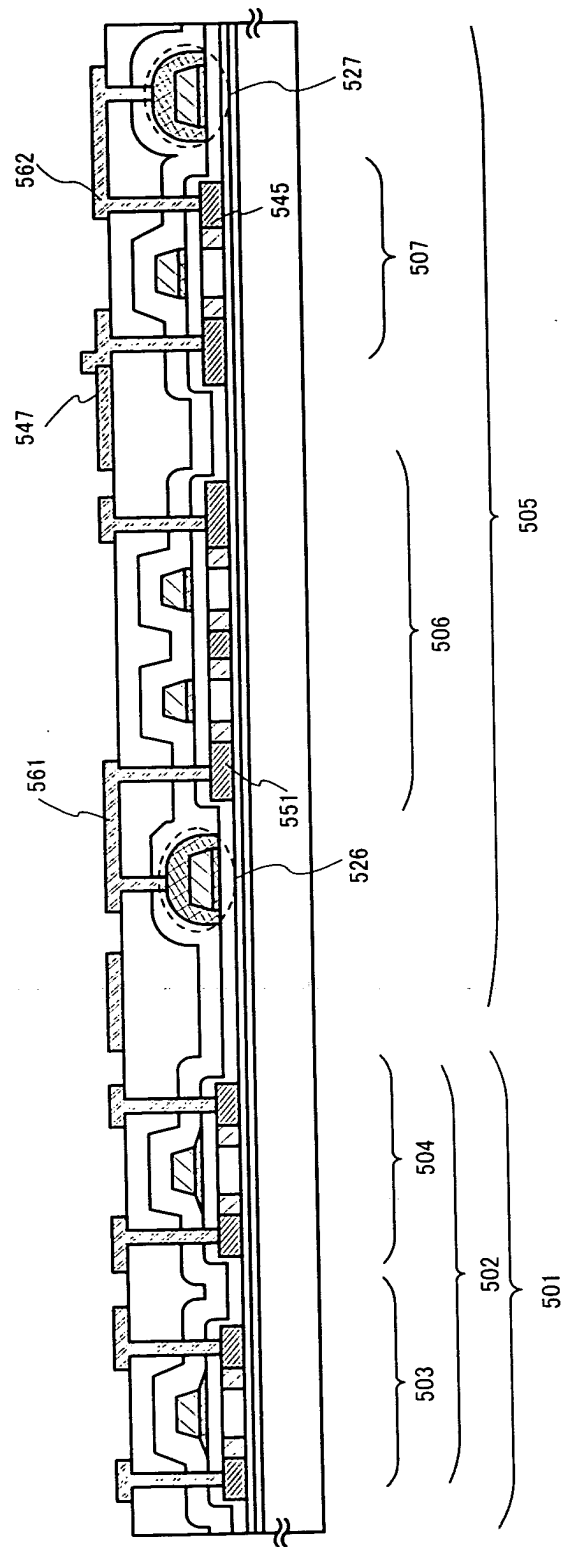


Fig. 10

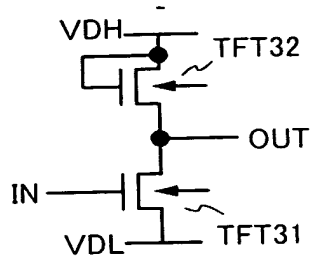


Fig. 11A EEMOS circuit

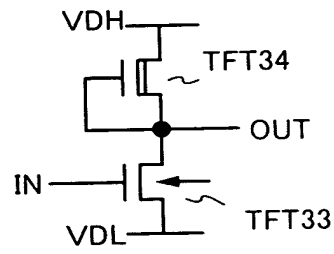


Fig. 11B EDMOS circuit

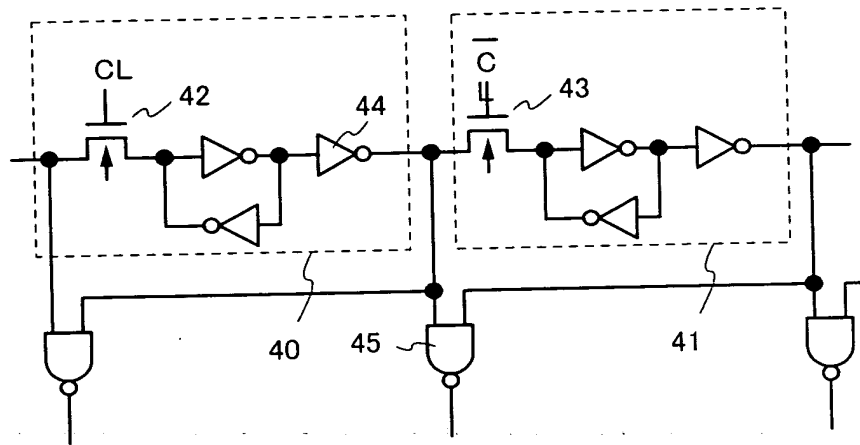


Fig. 12A

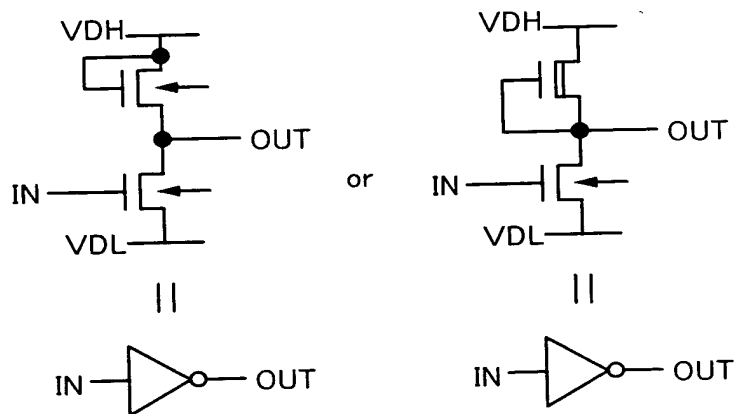


Fig. 12B

10022252 122001

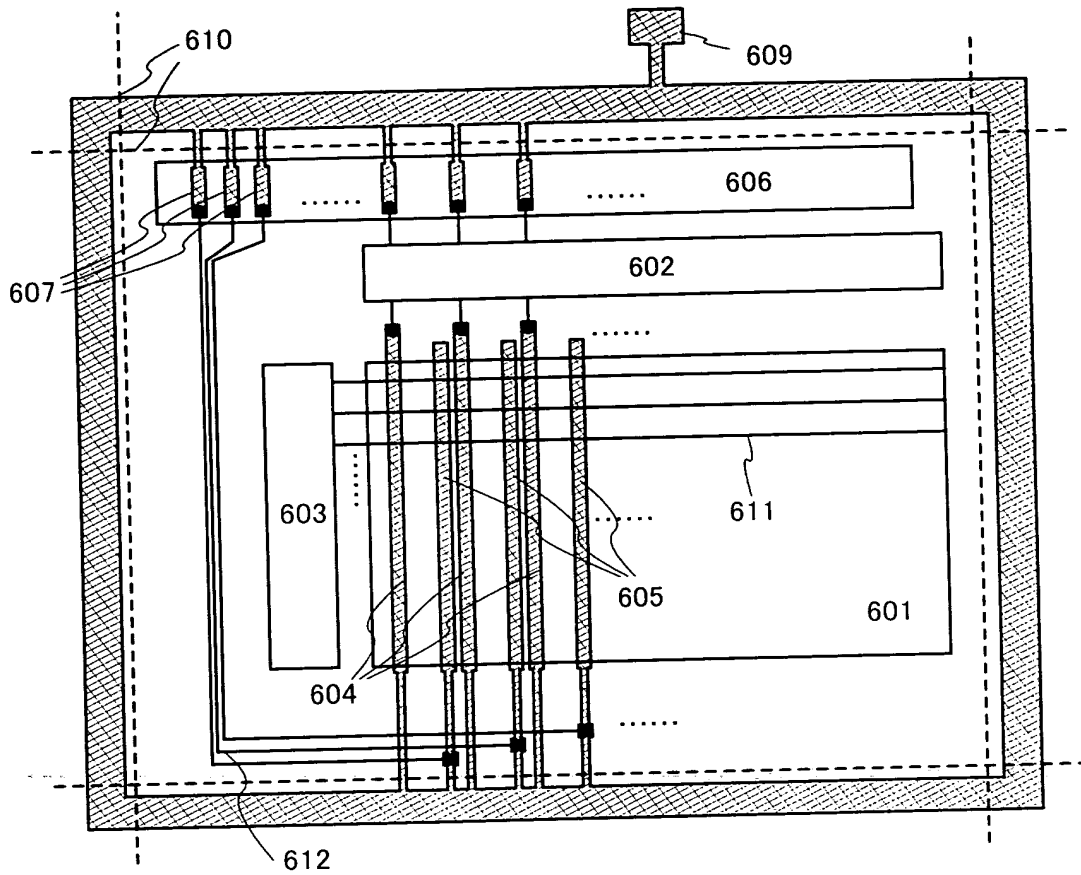


Fig. 13

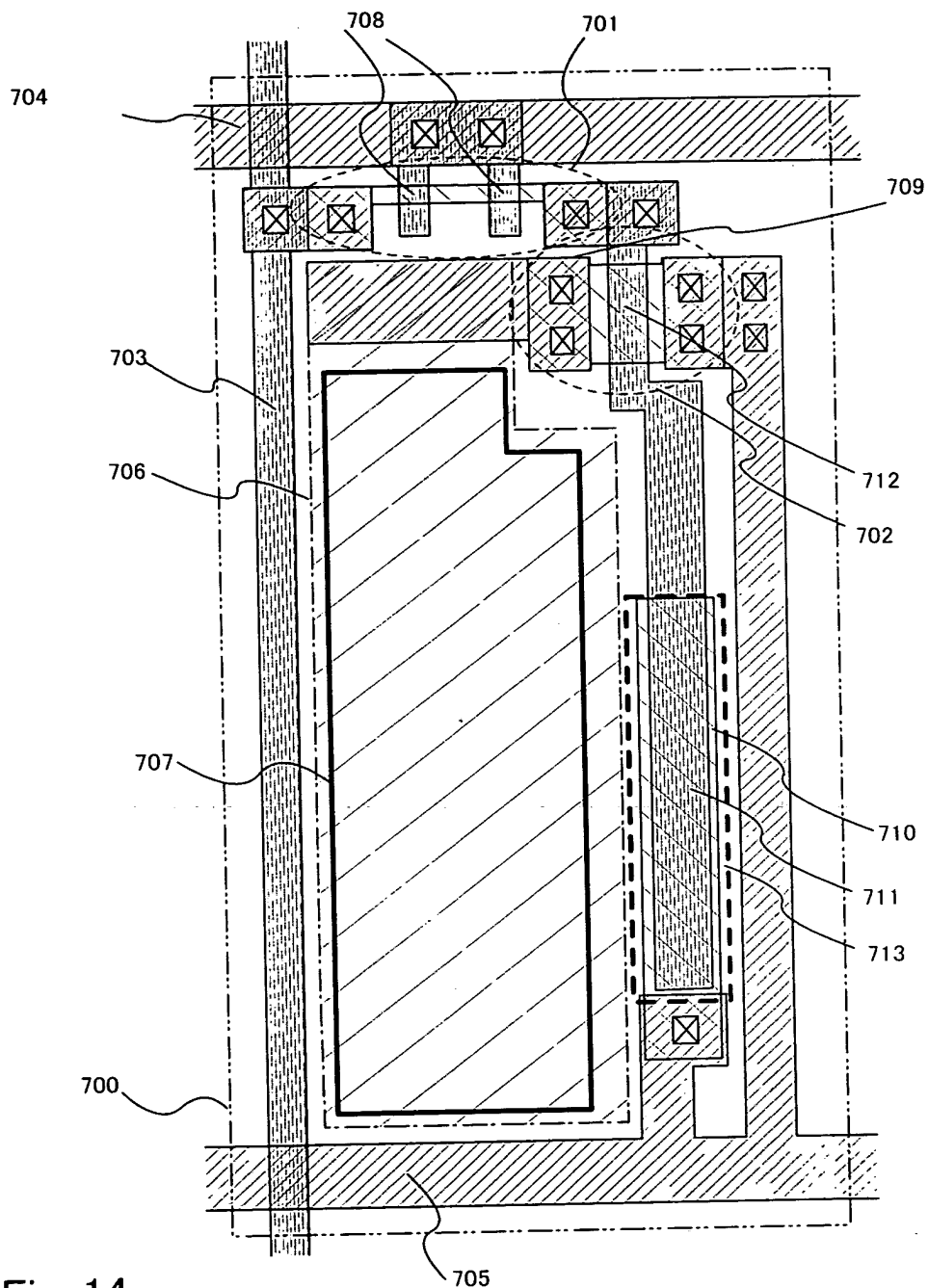


Fig. 14

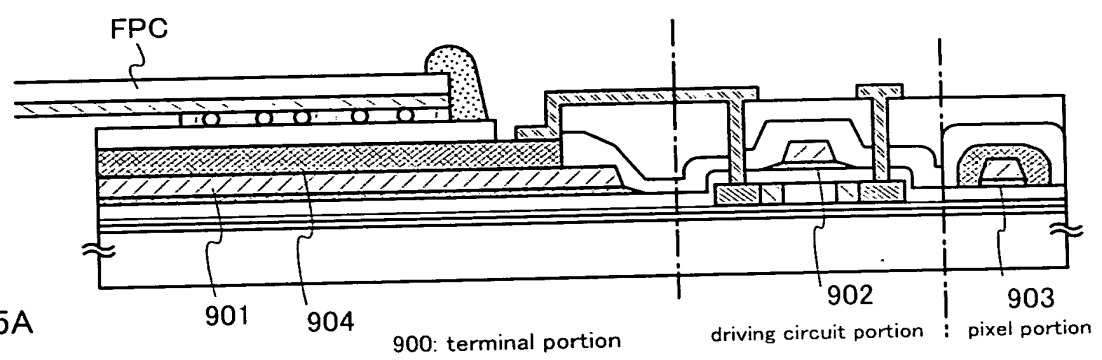


Fig. 15A

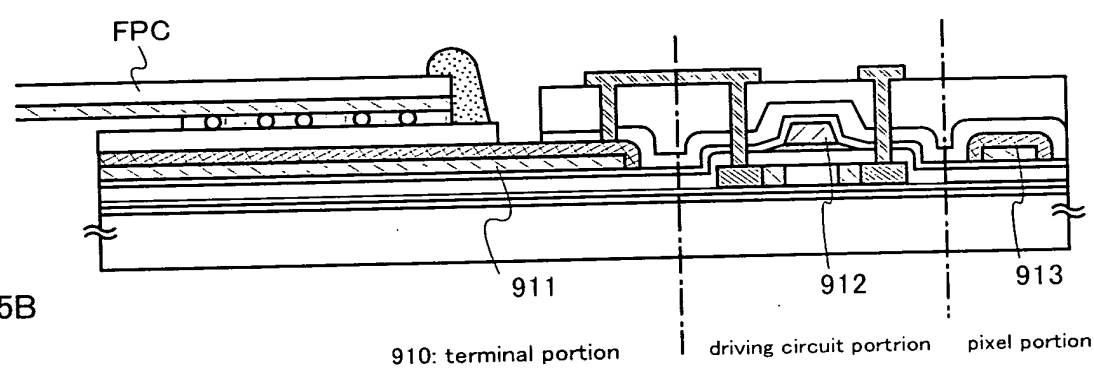


Fig. 15B

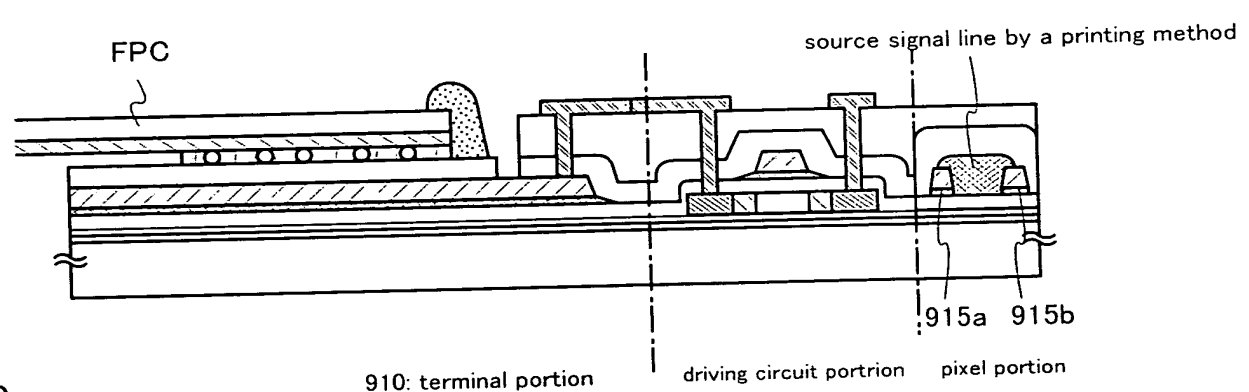


Fig. 15C

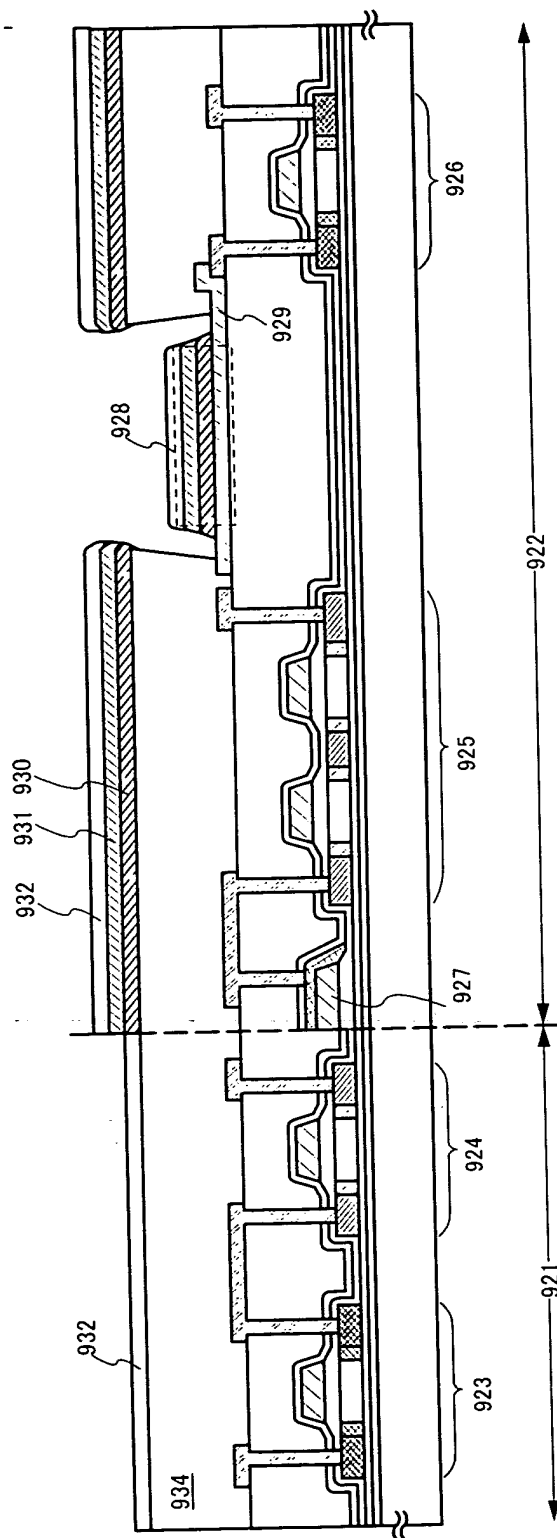


Fig. 16

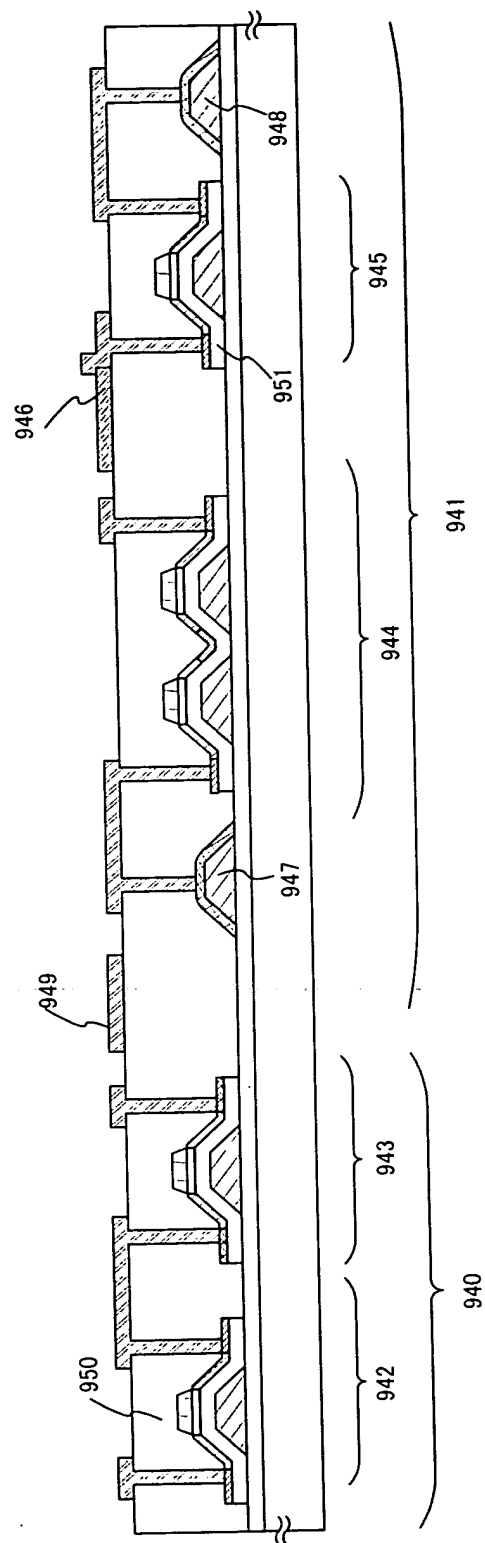


Fig. 17

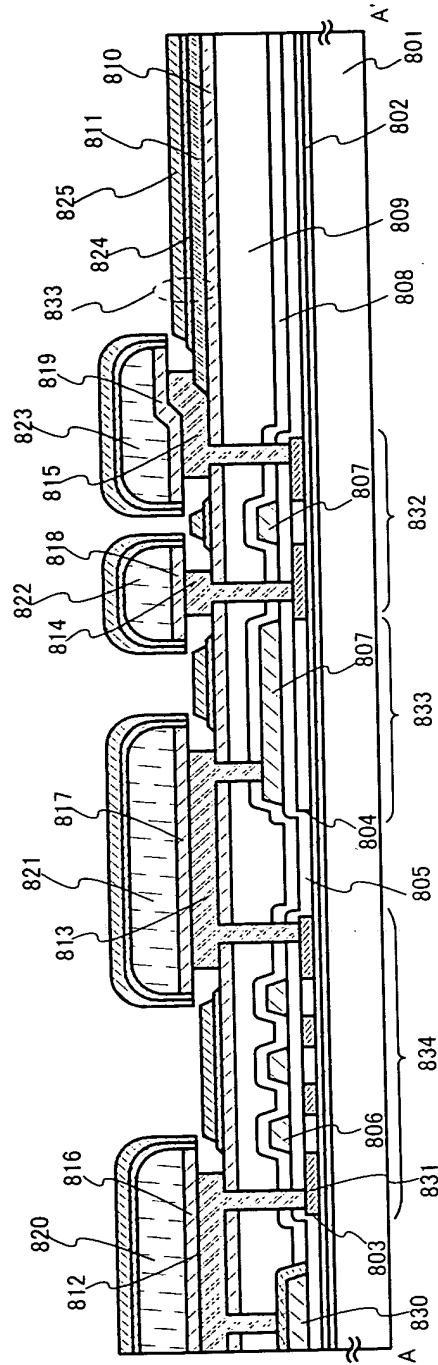


Fig. 18

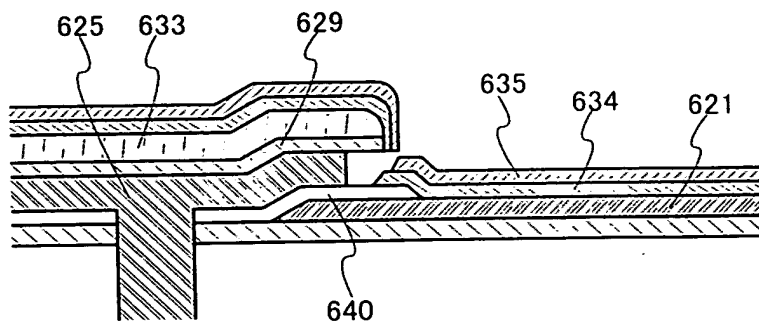


Fig. 19

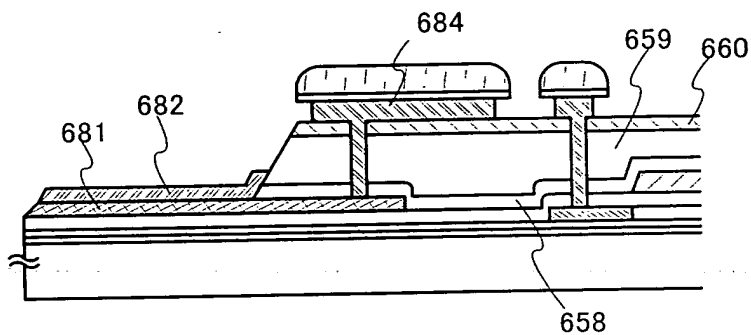


Fig. 20A

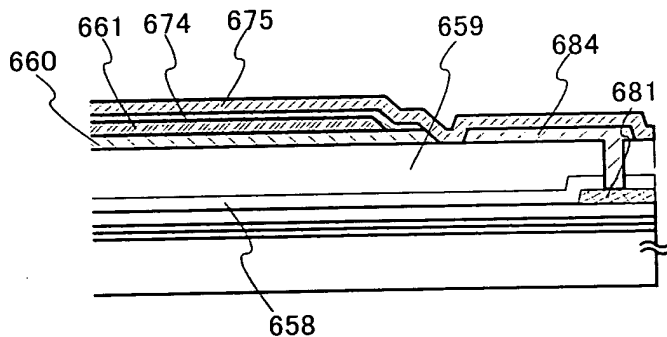


Fig. 20B

10022266.122001

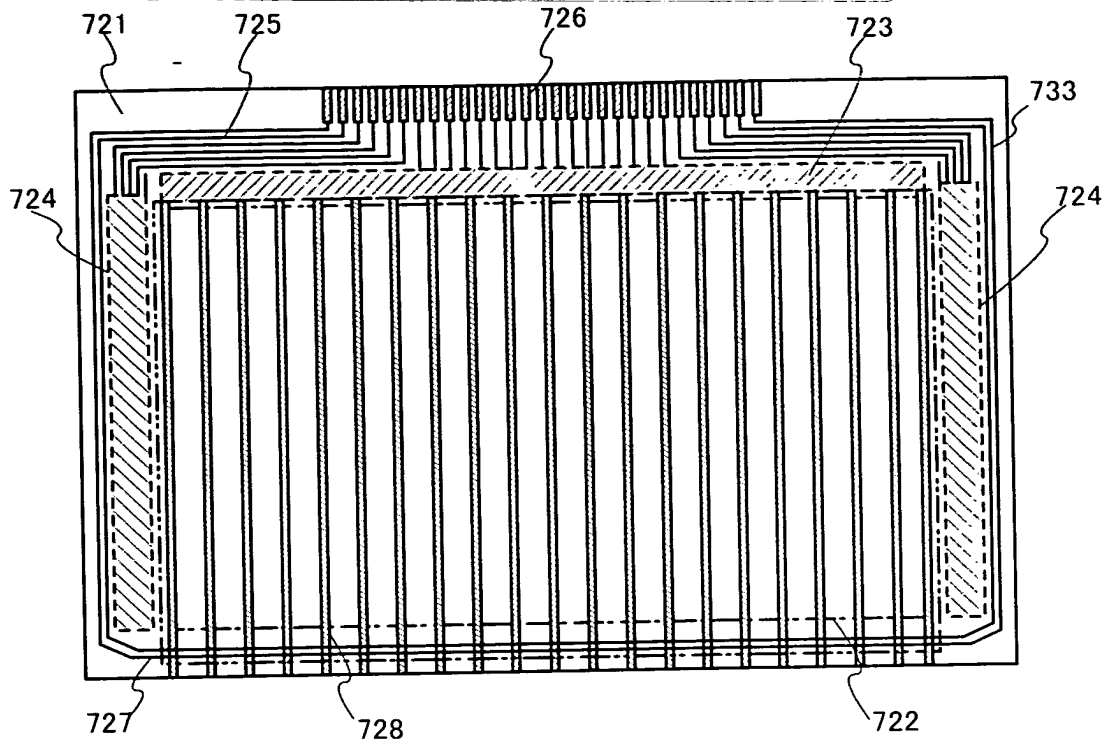


Fig. 21

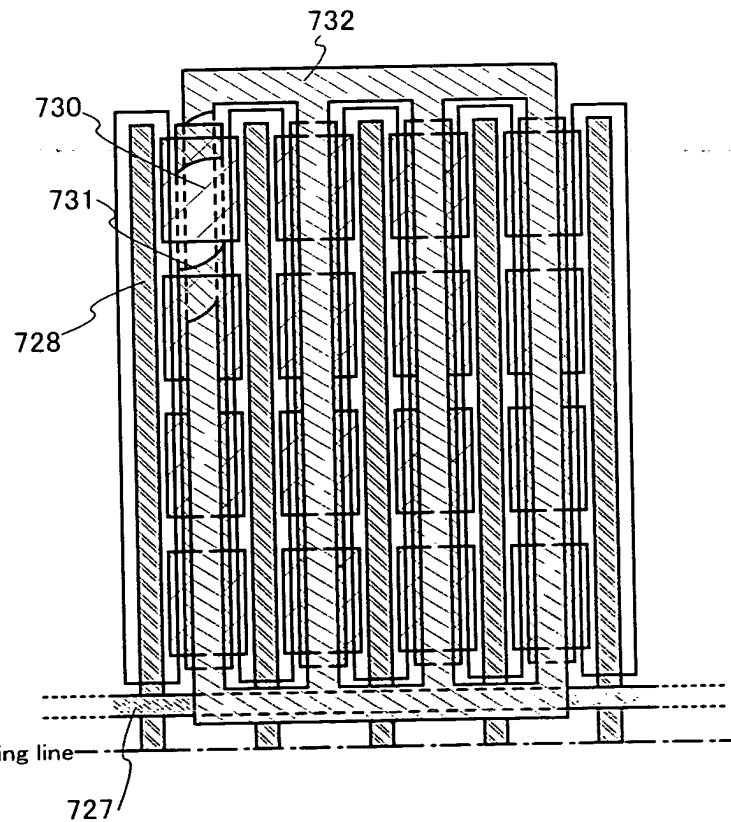


Fig. 22

1002262-12001

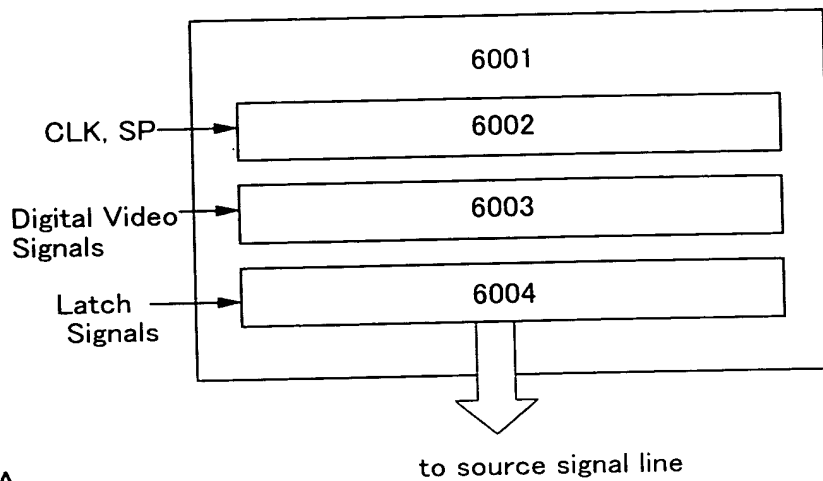


Fig. 23A

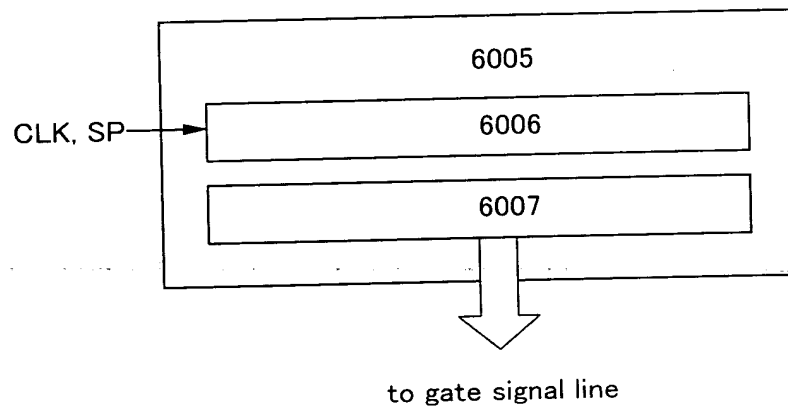


Fig. 23B

10022262.12001

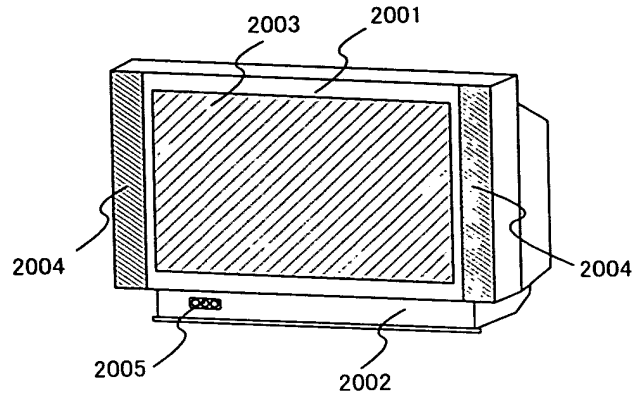


Fig. 24A

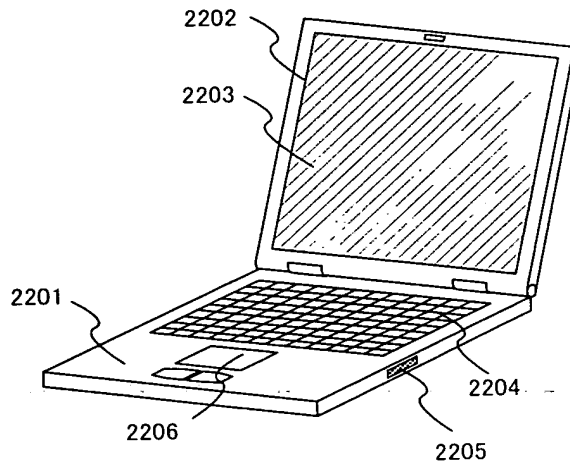


Fig. 24B

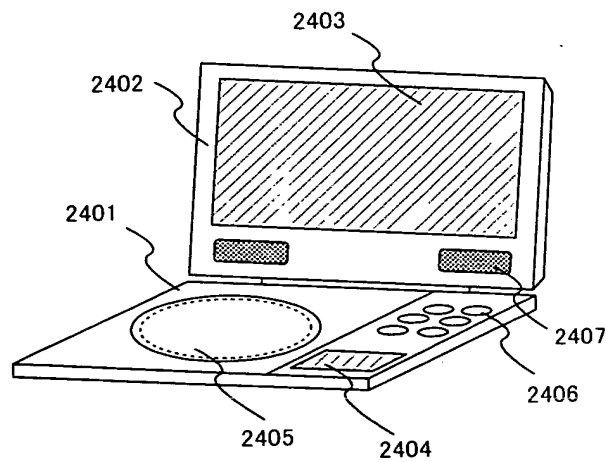


Fig. 24C

1002262-12001

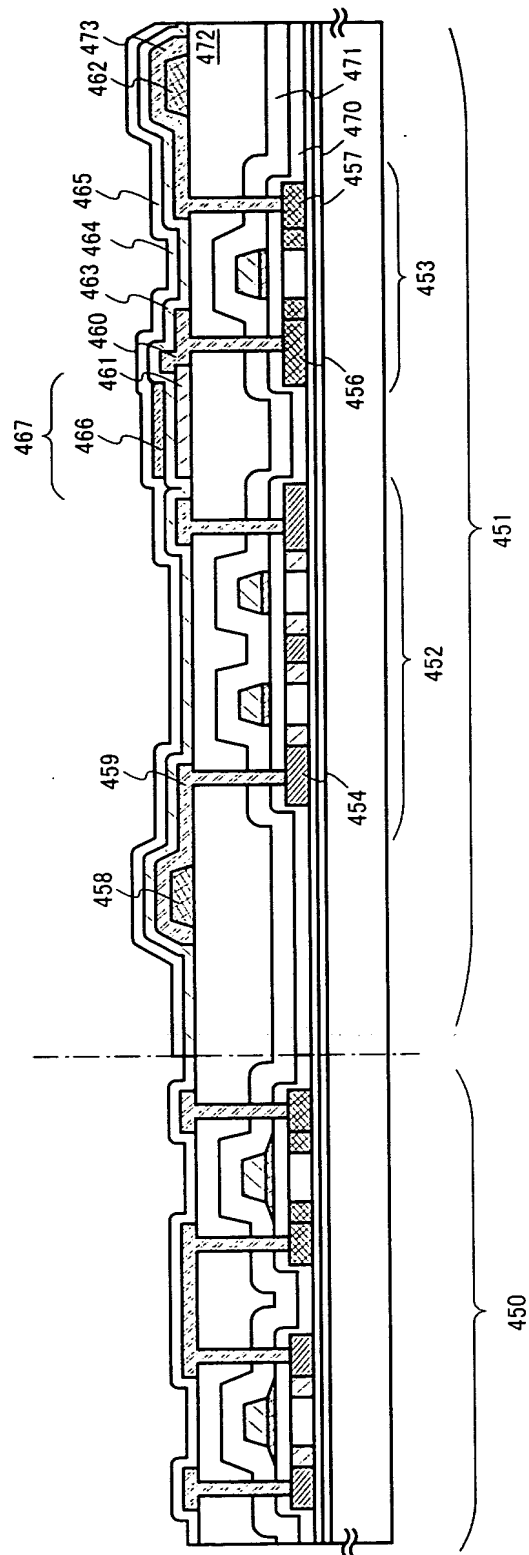


Fig. 25